

Electronic Acknowledgement Receipt

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Application Number:	10522262
International Application Number:	
Confirmation Number:	5683
Title of Invention:	Semiconductor structure for imaging detectors
First Named Inventor/Applicant Name:	Iiro Hietanen
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Filer:	William Edward Lewis
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	3505-2_IDS.pdf	206813	no	4
Warnings:					
Information:					
This is not an USPTO supplied IDS fillable form					
2	NPL Documents	3505-2_REF1.pdf	10323407	no	196
Warnings:					
Information:					
3	NPL Documents	3505-2_REF2.pdf	1085531	no	10
Warnings:					
Information:					
4	NPL Documents	3505-2_REF3.pdf	341172	no	4
Warnings:					
Information:					
5	NPL Documents	3505-2_REF4.pdf	419777	no	4
Warnings:					
Information:					
6	NPL Documents	3505-2_REF5.pdf	505837	no	6
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Total Files Size (in bytes):					12882537

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.